OPPA 10.17.02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

oplicant:

SHAEFFER et al.

Examiner:

Lugo, D.B.

Serial No.:

09/310,598

Group Art Unit:

2634

Filed:

May 12, 1999

Docket No.:

STFD.005PA

Title:

NOISE-REDUCING ARRANGEMENT AND METHOD FOR SIGNAL

PROCESSING

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence and the papers, as described hereinabove, are being deposited in the United States Postal Service, as first class mail, in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on October 2 2002.

OFFICE ACTION RESPONSE

RECEIVED

Assistant Commissioner for Patents Washington, D.C. 20231

OCT 1 0 2002

Sir:

Technology Center 2600

In response to the Office Action mailed July 2, 2002, please consider the following and remarks in connection with the above application.

In the Drawings

Enclosed please find a substitute drawing sheet with proposed changes to Figure 5 shown in red ink.

In the Claims

Please replace Claims 1-2, 9-10, 18 and 24 as indicated. The changes to the claims may be found on the attached sheets.

1. (Amended) In a communication arrangement having analog circuitry and having digital signal processing circuitry clocked sufficiently fast to generate noise, the analog circuitry susceptible to processing corrupted data due to the noise coupled thereto, a method for reducing noise passed from the digital signal processing circuitry. comprising the steps of: